Title:

LOAD BALANCING METHOD AND SYSTEM BASED ON ESTIMATED ELONGATION

RATES

Appl. No.:

Unassigned

Filing Date:

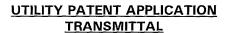
October 6, 2000

Examiner:

Unassigned

Art Unit:

Unassigned



Commissioner for Patents Box PATENT APPLICATION Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. § 1.53(b) is the nonprovisional utility patent application of:

Hidehito KUBO

Enclosed are:

- [X] Specification, Claim(s), and Abstract (82 pages).
- [X] Informal drawings (9 sheets, Figures 1-9).
- [X] Declaration and Power of Attorney (2 pages).
- [X] Assignment of the invention to NEC CORPORATION.
- [X] Assignment Recordation Cover Sheet.





[X] Preliminary Amendment.

The filing fee is calculated below:

	Claims	Included in Basic Fee			Extra				Fee
	as Filed				Claims	Rate			Totals
Basic Fee							\$710.00		\$710.00
Total Claims:	46		20		26	X	\$18.00	= '	\$468.00
independents:	9		3	_ =	6	×	\$80.00	=	\$480.00
Assignment Recording Fee per property + \$40.00							=	\$40.00	
							SUBTOTAL:	=	\$1698.00
[]	Small	Enti	ty Fees	Apply	/ (subtra	ct ½	of above):	= '	\$0.00
					TOT	AL F	ILING FEE:	=	\$1698.00

- [X] A check in the amount of \$1698.00 to cover the filing fee is enclosed.
- [] The required filing fees are not enclosed but will be submitted in response to the Notice to File Missing Parts of Application.
- [X] The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

Date: October 6, 2000

FOLEY & LARDNER
Washington Harbour
3000 K Street, N.W., Suite 500
Washington, D.C. 20007-5109
Telephone: (202) 672-5407
Facsimile: (202) 672-5399

David A. Blumenthal
Attorney for Applicant
Registration No. 26,257

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Hidehito KUBO

Title:

LOAD BALANCING METHOD AND SYSTEM BASED ON ESTIMATED

ELONGATION RATES

Appl. No.:

Unassigned

Filing Date:

October 6, 2000

Examiner:

Unassigned

Art Unit:

Unassigned

PRELIMINARY AMENDMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

Prior to examination of the above-identified application, Applicant respectfully requests that the following amendments be entered into the application:

In the Specification:

Please amend the specification as follows:

Page 24, line 2,

replace "program" with --processing processes--;

line 23,

replace "program" with --processing processes--.

Page 71,

line 5,

delete "CPU of the".

REMARKS

The foregoing amendments are being made to the specification for clarification purposes. No new matter has been added.

Respectfully submitted,

Date: October 6, 2000

FOLEY & LARDNER
Washington Harbour
3000 K Street, N.W., Suite 500
Washington, D.C. 20007-5109
Telephone: (202) 672-5407
Facsimile: (202) 672-5399

David A. Blumenthal Attorney for Applicant Registration No. 26,257

15

LOAD BALANCING METHOD AND SYSTEM BASED ON ESTIMATED ELONGATION RATES

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to a system for executing transaction processing by sharing load among a plurality of computers, nodes, or processors, that are connected to each other to form a relatively close group such as a cluster, and more particularly to a system and method for balancing transaction processing loads among the computers.

2. Description of the Related Art

In a system including a plurality of processing devices (computers), a load balancing method is designed to provide the maximum performance of the system by appropriately distributing a large number of successive arrivals of messages each requiring small-scale processing over these multiple computers. Since the processing required for each message is small in scale, a computer to execute the processing of a message is generally decided on arrival and the selected computer performs and finishes the processing without migrating it to another computer in progress.

AND SON AND REMARKS OF THE PARTY.

5

10

20

25

FQ5-489

2

Moreover, since the processing is interactive, the ultimate objective of load balancing is to minimize the mean and variation of response times. In general, when a message arrives, a computer with lightest load is selected as a computer to execute the processing of the message.

Here, what is to be selected as "load index" is very important. Conventionally, as load index, the CPU utilization of a computer, the number of processing jobs at a computer, records of response time in the resent past and the like have been used, individually and separately, or in combination.

A conventional example has been disclosed in Japanese Patent Application Unexamined Publication No. 10-312365 (hereafter, called Prior Art 1). Here, the load states (here, CPU utilizations) of servers (computers) are measured and stored at regular time intervals. Based on the stored load states, a server with lightest load is selected as an executing computer that is put in charge of processing a new message. Beside, a terminal monitors the response time of processing at the server and, if the response time exceeds a predetermined value, then the terminal sends a path change request to change the executing computer to anther computer.

In this case, CPU utilization is used as a first load index. The CPU utilization is certainly a good index, but the measured value is a mean value of past certain time

3

interval and does not contain the effect of start/termination of the following processing or the like, so it is not so reliable as index representing the "actual state".

Particularly in the case of dynamic control, since the measured data at a time instant is used until the next measurement time, processing requests arriving meanwhile are sent intensively to a specific one server judged to be least loaded, resulting in seesawing load balancing.

As a second load index, the record of response time of processing in progress is used to determine whether the currently executing computer should be changed to another computer. The record of response time would be an effective index if the changeover of executing computer in progress were performed with a small overhead. However, it is impossible to use this index because there is no record of that processing itself when arriving.

Another conventional example has been disclosed in Japanese Patent Application Unexamined Publication No. 10-27168 (hereafter, called Prior Art 2). According to this conventional example, the processing time of the latest message is stored at each computer and is multiplied by the number of in-process messages on that computer to produce a load index value. When receiving a message, the load index values are calculated at all computers, and a computer with

10

15

20

FQ5-489

4

the smallest load index value is selected to process the message.

In this case, the problem is whether the processing time of the last processed message represents the processing times of messages on that computer. Such a processing time may reflect the congestion state of that computer and the job characteristics (net processing time, the ratio CPU use to I/O use time) of the last message processing. If the job characteristics are identical for all messages, they may be considered to give a yardstick as load index. However, in general situations where those of various job characteristics are mixed, there is a strong likelihood of misjudging if each processing time as it is reflects the load state.

Still another conventional example has been disclosed in Japanese Patent Application Unexamined Publication No. 7-302242 (hereafter, called Prior Art 3). Among many claims, a technique related to the present invention is described in claim 9 or paragraph Nos. 152 to 161 in the specification. Here, the load of a transaction processing section is detected periodically and the load history is memorized with the time. Then, the load trend Tr is calculated by the following expression:

Tr = (W2-W1)/(T2-T1).

In the case where a server has received a transaction
25 processing request, if it is judged that the processing load

20

FQ5-489

5

prediction value will not exceed a threshold value Wt after an elapse of time Ti, that is, Tr·Ti ≤ Wt, then the sever accepts the transaction processing request and, if not (Tr. Ti > Wt), then the server refuses it or requests the processing of another server with lighter load. In this example, the load is detected periodically and the detected load is used to determine, but what is concretely defined as "load" is not specified clearly all through the whole Publication. Though the provision of load index is a first important step for load balancing, it is not done. Besides, it is attempted to predict the load after an elapse of Ti by extrapolating linearly the past (it is seemed insufficient by Tr'Ti but it is apart from that matter), but it does not seem to be a good prediction. This kind of macro prediction might be effective concerning the load of the whole system. However, the future load state of each server is decided by micro movement such as the actual state, in-process transaction termination timing, and whether it accepts or not new processing, so it is undesirable to extend the past trend and take it for granted as it is.

Either of the above-described Prior Arts 1. 2 and 3 decides the computer in charge of processing a new message aiming to minimize the processing time of the arrived message itself. However, there is not necessarily a guarantee that

141

10

15

20

25

such individual optimization system is directly linked to the optimization of the whole system.

A still further conventional example has been described in pp. 225 to 232 of "Optimal Load Balancing in Distributed Computer Systems" (H. Kameda et al.) published by Springer, 1977 (hereafter, called Prior Art 4). Looking at one CPU, a premised node model may be shown in Fig. 2. An arrival job at a node repeats the use of CPU (computer 1 in Fig. 2) and disks, and leaves upon the termination of processing. The time required for this processing is the response time of the job. Since a plurality of jobs are processed in parallel, a queue is generated at the CPU. The other CPUs are provided similarly as the illustrated computer i, and all CPUs take the same access time for an access to any disks. Here, the following two expressions are shown as load index:

$$fi = si (ni + 1)^2$$
 (1); and

$$Fi = si (ni + 1)$$
 (2),

where, f and F are load indexes, i is a computer number, s is the mean net service time in CPU for jobs, and n is the number of jobs in the CPU system. These expressions are deduces as values the smaller the better so as to minimize the mean response time in a sense, based on a relation realized concerning mean values in the equilibrium state for a CPU system, in an open queuing network model proposed in the queuing theory. In fact, the expression (2) represents

10

15

25

FQ5-489

the mean time in a CPU system, and the mean response time is obtained by adding the mean time in the input/output system to that in the CPU system. It has been shown that these are optimal in a sense as an index for static load balancing.

7

However, the advantage of dynamic control resides in the capability of control depending on the situation of that time, and it is meaningless for si and ni unless the actual value is used in place of mean value in the equilibrium state. actual value of ni can be measured, however, si reflecting characteristics of job mix being executed on the computer i can not be measured directly. In the evaluation described in this paper, the mean value of the whole is used as si. mean value of the whole may well be used if the processing to be executed is of single type from the viewpoint of job characteristics and, moreover, if the variation of job characteristics is narrow. However, the merit of dynamic control will mostly be lost by the use of the mean value of the whole, if it is of single type but the variation of job characteristics is wide, or in the case where multiple types of processing providing different characteristics are mixed, which appears much closer to reality.

A first problem of the prior arts is that the load state of each computer that is used to determine the load balancing cannot be sufficiently grasped. Conventionally, the number of in-process transactions, CPU utilization and the likes

¢

have been used respectively and individually or in combination as load index. However, it cannot be said that they reflect sufficiently the system congestion state, taking into consideration job characteristics including the CPU/input output use rate in a group of in-process transactions at that time.

In a system where a very large number of small scale processing requests arrive as in the case of transaction processing, it is necessary to exactly grasp the load state in a short time interval. However, the trend has been to reduce the collection frequency because the overhead of data collection increases in the distributed computer system. Besides a device to use good data of low overhead, it is required to make good use of clustered computer's capability to collect data with high speed/low overhead.

A second problem of the prior arts is that the systemwide optimization on average of response time and
minimization of variations cannot be always obtained. An
optimal computer that is expected to process the arrived
transaction in the shortest time at that time is selected as
a computer in charge of the processing. However, such an
individual optimization scheme does not necessarily ensure
the system-wide optimization.

10

15

20

٥

SUMMARY OF THE INVENTION

An object of the present invention is to provide an effective load index serving as criterion for selecting an execution destination computer in charge of processing a processing request among a plurality of computers.

Another object of the present invention is to provide a low-overhead dynamic load balancing method capable of keeping the system-wide mean and variation of response times small by balancing the transaction processing load among the computers.

According to the present invention, a load balancing method in a system comprising a plurality of computers for processing transaction processing requests originating from a plurality of terminals, includes steps of: a) estimating load states of respective ones of the computers; b) determining estimated elongation rates of processing time for respective ones of the computers based on the estimated load states; c) calculating load indexes of respective ones of the computers from the estimated elongation rates; and d) determining load distribution among the computers based on the load indexes.

A load balancing method includes steps of: a) estimating elongation rates of processing time for respective ones of the computers, wherein an elongation rate is a ratio of a processing time required for processing a transaction to a net processing time which is a sum of CPU time and an

The transfer of the state of th

10

15

20

25

FQ5-489

10

input/output time for processing the transaction; b) calculating load indexes of respective ones of the computers based on the estimated elongation rates; and c) selecting a destination computer from the computers based on the load indexes, wherein the destination computer having a minimum one among the load indexes.

A load balancing system includes: a plurality of terminals, each of which originates a transaction processing request; a plurality of computers, each of which processes a plurality of transaction processing requests originating from a plurality of terminals in parallel; a load estimator for estimating load states of respective ones of the computers; a load data memory for storing the estimated load states; and an execution computer selector for selecting one of the computers as an execution computer to be put in charge of processing a transaction processing request based on load indexes of respective ones of the computers, wherein the load indexed are calculated from estimated elongation rates of processing time for respective ones of the computers, wherein the estimated elongation rates are determined based on the estimated load states.

An elongation rate of a processing time is defined as a ratio of the response time of a job processing process (a processing time including a queuing time) to the net processing time (total time actually using resources

20

25

FQ5-489

11

including CPU and file devices). In the case of all the computers providing the same processing speed, a processing process can be completed faster by a computer having a lower elongation rate and, therefore, the response time thereof can be reduced. An estimated elongation rate reflects program characteristics as group in operation (including not only the CPU use characteristic but also CPU-I/O use characteristic) in a job mix that is a group of processes in operation on the concerned computer.

The elongation rate of processing time in each computer is estimated based on measured load data obtained by measuring, at a certain time cycle, the number of in-process transactions and the number of job processing processes staying in the CPU system, or, the number of in-process transactions and the CPU utilization.

The load data of each of the computers may be measured at constant intervals to produce a sequence of load data.

And a current load state of each of the computers may be estimated based on the sequence of load data.

The estimated load states may be corrected using the current numbers of in-process transactions for respective ones of the computers to produce corrected estimated load states, and estimated elongation rates of processing time for respective ones of the computers may be determined based on the corrected estimated current load states.

[]

THE SAME SAME SECTION OF A

IJ1

'n.

ii lee k

10

12

The estimated elongation rate may be used as a load index of each of the computers. The estimated elongation rate may be one of a before-scheduling estimated elongation rate and an after-scheduling estimated elongation rate, wherein the before-scheduling estimated elongation rate is an estimated elongation rate calculated from a corresponding estimated load state before the transaction processing request is allocated to respective ones of the computers, and wherein the after-scheduling estimated elongation rate is an estimated elongation rate calculated from a corresponding estimated load state after the transaction processing request is allocated to respective ones of the computers.

A load index of the computer may be determined such that the estimated elongation rate of each of the computers is multiplied by a current number of in-process transactions in the computer to produce a total estimated elongation rate of the computer, and the total estimated elongation rate is determined as a load index of the computer.

A load index of the computer may be determined such that

the estimated elongation rate of each of the computers is

multiplied by a current number of in-process transactions in

the computer to produce a total estimated elongation rate of

the computer, a total estimated elongation rate difference

between an after-scheduling total estimated elongation rate

and a before-scheduling total estimated elongation rate of

10

20

25

FQ5-489

13

each of the computers is calculated, and the total estimated elongation rate difference is determined as a load index of the computer.

According to another aspect of the present invention, a load balancing method in a system comprising a load balancing device for distributing transaction processing requests originating from a plurality of terminals to a plurality of execution computers, includes steps of: at the load balancing device, a) receiving load data from each of the execution computers at regular intervals, the load data including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the execution computer; b) estimating load states of respective ones of the computers based on load data received from the execution computers; c) determining estimated elongation rates of processing time for respective ones of the computers based on the estimated load states; d) calculating load indexes of respective ones of the computers from the estimated elongation rates; and e) determining load distribution among the computers based on the load indexes.

According to further another aspect of the present invention, a load balancing method includes steps of: at each of the computers, a) estimating a load state of the computer based on load data measured at regular intervals including a number of in-process transactions and one of a CPU

5

Han I I took that they I'll

'n.j

]= L

20

25

14

utilization and a number of job processing processes staying in a CPU system of the computer; b) storing the load state of the computer and a load state received from another computer in a load state memory; c) transferring the load state of the computer to all other computers; d) when receiving a transaction processing request from a terminal, determining estimated elongation rates of processing time for respective ones of the computers based on the load states stored in the load state memory; e) calculating load indexes of respective ones of the computers from the estimated elongation rates; f) determining based on the load indexes whether the transaction processing request should be processed by the computer or transferred to another computer; g) when it is determined that the transaction processing request should be transferred to another computer, determining a destination computer among the computers based on the load indexes to transfer it to the destination computer; and h) when it is determined that the transaction processing request should be processed by the computer, processing the transaction processing request.

According to still another aspect of the present invention, a load balancing method includes steps of:

at the interim load balancing device, a) setting a predetermined distribution scheme; b) when receiving a transaction processing request from a terminal, selecting an interim destination execution computer from the execution

[]

5

10

20

25

FQ5-489

15

computers according to the predetermined distribution scheme;
c) sending the transaction processing request to the interim
destination execution computer;

at each of the execution computers, d) estimating a load state of the execution computer based on load data measured at regular intervals including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the execution computer; e) storing the load state of the execution computer and a load state received from another execution computer in a load state memory; f) transferring the load state of the execution computer to all other execution computers; g) when receiving the transaction processing request from the interim load balancing device, determining estimated elongation rates of processing time for respective ones of the execution computers based on the load states stored in the load state memory; h) calculating load indexes of respective ones of the execution computers from the estimated elongation rates; i) determining based on the load indexed whether the transaction processing request should be processed by the execution computer or transferred to another execution computer; j) when it is determined that the transaction processing request should be transferred to another execution computer, determining a final destination computer among the execution computers based on the load indexes to transfer it to the

5

THE WEST

15 15 15

20

10

16

final destination computer; and k) when it is determined that the transaction processing request should be processed by the execution computer, processing the transaction processing request.

The predetermined distribution scheme may be a static distribution method such that the terminals are previously divided into a plurality of groups and an interim destination execution computer is determined depending on which one of the groups a request originating terminal belongs to.

The predetermined distribution scheme may be a static round distribution method such that the execution computers are sequentially and repeatedly selected as an interim destination execution computer in the arrival order.

The predetermined distribution scheme may be a quasistatic method such that after receiving load data from each
of the execution computers at regular intervals, the load
data including a number of in-process transactions and one of
a CPU utilization and a number of job processing processes
staying in a CPU system of the execution computer, the
predetermined distribution scheme is determined based on the
load data so that a transaction processing load is balanced
among the execution computers.

BRIEF DESCRIPTION OF THE DRAWINGS

FQ5-489

17

- Fig. 1 is a block diagram showing the configuration of a transaction load balancing system according to a first embodiment of the present invention;
- Fig. 2 is a diagram showing a system model for any one 5 computer i;
 - Fig. 3 is a diagram showing a table of data used for calculating the value of load index;
 - Fig. 4A is a flow chart showing the operation of an execution computer selector in the first embodiment;
 - Fig. 4B is a flow chart showing the operation of an execution computer selector in a transaction load balancing system according to a second or third embodiment of the present invention;
- Fig. 5 is a diagram a table of eight kinds of load used in the present invention;
 - Fig. 6 is a block diagram showing the configuration of a transaction load balancing system according to the second embodiment of the present invention;

Con Fill top hill New Fill

15

18

Fig. 7 is a block diagram showing the configuration of a transaction load balancing system according to the third embodiment of the present invention;

Fig. 8 is a graph showing the mean response time, which is a result of simulation; and

Fig. 9 is a graph showing a standard deviation of response time, which is a result of simulation.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

FIRST EMBODIMENT

10 1.1 System configuration

Referring to Fig. 1. a computer group 1 is composed of n computers 1.1 through 1.n (generally denoted by 1.x), each of which is a program-controlled computer. The computer group 1 is connected to a load balancing device 2 and a file device group 4 through a high-speed channel 3. The load balancing device 2 is further connected to a terminal group 5 through a communication network 51.

In the computer group 1, a computer 1.x (x = 1, 2, \dots , or n) includes a load data measurement section 1.x1, a

20

FQ5-489

19

transaction processing section 1.x2, and a communication section 1.x3. The transaction processing section 1.x2 includes a plurality of job processing processes (not shown).

The load balancing device 2 includes a communication section 21, a load data memory 6, an execution computer selector 7, and a load data measurement section 8. The computers 1.1 to 1.n do not share a main memory but the file device group 4 via the high-speed channel 3 with equivalent performance conditions.

A single transaction is processed as follows. A terminal device belonging to the terminal group 5 transmits a message, which is a transaction processing request to the load balancing device 2 via the communication network 51. The load balancing device 2 selects one of the computers 1.1 through 1.n as a computer in charge of processing the received message. Assuming that the selected computer is denoted by a general reference numeral 1.i (i = 1, 2, ..., n), the load balancing device 2 sends the message to the selected computer 1.i via the high speed channel 3. The selected computer 1.i, when receiving the message, executes the transaction processing thereof, produces a response message, and sends it back to the request-originating terminal device through the high-speed channel 3, the load balancing device 2, and the communication network 51 in reverse.

Garle State Mine II'll

ii l= k

10

15

20

25

20

Here, load data measurement section 1.x1, transaction processing section 1.x2 and communication section 1.x3 provided on a computer 1.x are each implemented by programs stored in machine readable recoding media such as CD-ROM, magnetic disk, semiconductor memory, or the like (not shown), the respective programs running on each computer 1.x to implement them when the computer group 1 is started up or at other moments.

Similarly, the communication section 21, the load data memory 6, execution computer selector 7, and the load data measurement section 8 provided on the load balancing device 2 are implemented by programs stored in machine readable recoding media such as CD-ROM, magnetic disk, semiconductor memory, or the like (not shown), the respective programs running on a computer of the load balancing device 2 to implement them when the computer is started up or at other moments.

Since the computers 1.1 to 1.n have the same function, they will be represented by a computer 1.i (i = 1, 2, ..., n). The load data measurement section 1.il on the computer 1.i periodically measures load data of the computer 1.i thereof, and sends the measured load data to the load balancing device 2. Upon the reception of processing request message received from the load balancing device 2 through the communication section 1.i3, the transaction processing section 1.i2

The period of th

20

25

allocates the received processing request to the job processing process under its own control so that the requested processing is performed. The job processing process repeatedly uses the CPU and accesses files on the file device group 4 to execute the program, produces a response message when the processing is terminated, and sends it to the load balancing device 2 via the communication section 1.i3.

The load data measurement section 8 periodically receives the load data from the respective computers 1.1 through 1.n via the communication section 21, processes the received load data to produce estimated load data, and stores it to the load data memory 6. On the other hand, the load data measurement section 8 receives notification of occurrence of a transaction processing request and an arrival of a response message from the communication section 21, and updates a part of data on the load data memory 62

The execution computer selector 7 receives a transaction processing request message via the communication section 21 and calculates an estimated elongation rate for each computer 1.i using the estimated load data of the computers stored in the load data memory 6. Based on the estimated elongation rate for each computer, the execution computer selector 7 selects from the computers 1.1 - 1.n an execution computer to be put in charge of processing the received transaction

15

FQ5-489

processing request. When the execution computer has been determined, the execution computer selector 7 instructs the communication section 21 to send the transaction processing request message to the selected computer through the high-speed channel 3.

22

1.2 Operation

Now, an operation of the first embodiment will be described in detail referring to Figs. 1-5.

In Fig. 1, upon the reception of a transaction processing request message from the load balancing device 2 through the communication section 1.13, the transaction processing section 1.12 allocates the transaction processing request to the job processing process under its own control, and makes it execute the transaction processing depending on the request. The transaction processing section 1.i2 is capable of processing a plurality of transactions, that is, a plurality of job processing processes on multiprogramming state, and thus improves the response time and the use efficiency of resources. A job processing process repeatedly uses the CPU and gains access to the file device group 4 to execute a corresponding job program. When terminating the processing, the transaction processing section 1.12 sends the response message to the load balancing device 2 through the communication section 1.i3, and then terminates the process.

The resource use characteristic of a job processing process can be determined by the CPU time and the number of input/output accesses, but it is different from one transaction to another. Since pluralities of processes are made to run in parallel, they may access the same resource concurrently. Therefore, it is supposed that a process queue will be formed at least in front of the CPU. In general, the higher the CPU utilization, the longer the queue. As for the input/output, all the computers originating an access request have the same performance condition and therefore it is supposed that the processing time including queuing time is not different among the computers.

Referring to Fig. 2, it is assumed that Ni represents the number of in-process job processing processes on the computer i. It corresponds to the number of transactions being processed on said computer. It is further assumed that Pi represents the number of job processing processes in the CPU system, and so, Pi is smaller than or equal to Ni. It is the total number of job processing processes running on the CPU and processes waiting for CPU allocation (ready state). Since the file device group 4 is shared among all the computers and the access performance is equal for all computers, all the computers are supposed to have the same performance as the computer i.

20

25

FQ5-489

24

The load data measurement section 1.il on the computer 1.1 measures periodically the number of job programs. Pi, staying in the CPU system at that moment or the CPU utilization, Ri, from the previous measurement timing to the current measurement timing, and the number of in-process job processing processes, Ni, at that moment. The load data measurement section 1.il sends the measured data, Pi/Ri and Ni, as periodically measured load data, to the load balancing device 2 via the communication section 1.i3. Taking into consideration accuracy/overhead tradeoffs because the measurement interval has an effect on the load balancing accuracy, the measurement interval is preferably about 100 milliseconds or less and, further preferably about 10 milliseconds, for an ordinary transaction processing. Which one of Fi and Ri is used may be decided for each execution system. Here, the case where Pi is used is called P method. and the case where Ri is used is called R method?

Referring to Fig. 3, a computer number T1 shows the identification of a computer operating in the system, and a computer is managed based on data contained in a row of the table. The load data measurement section 8 periodically receives, via the communication section 21, values of the number of job programs Pi in the CPU system or the CPU utilization Ri from the previous measurement timing to the current measurement timing and the number of in-process job

20

FQ5-489

25

processing processes Ni at that moment, from the load data measurement section 1.i1. Then, the load data measurement section 8 processes the received data, Pi/Ri and Ni, to produce the estimated value Pei of Pi or the estimated value Rei of Ri and the estimated value Nei of Ni as estimated load data. In the case of the R method, Pei can be calculated from Rei. The Pei is stored in the column T4 and the Nei is stored in the column T3 for the computer number i.

The number of current in-process transactions, Npi, is stored in the column T2 for the computer number 1 of the table. This value Npi is updated and stored in the table by the load data measurement section 8 when notified of transmission of a transaction processing request (transaction start) and arrival of a response message (transaction termination) from the communication section 21. Therefore, the value Npi is a reliable measured value.

1.3 Estimated value calculation

The measured values are not used as they are but converted into estimated values in order to compensate for low sampling reliability by comprehensively integrating the past data.

More specifically, an estimated value is obtained using the following relation. Assuming that m is a measured value, e is an estimated value, and n is the times of measurements:

25 $e(n) = a \cdot m(n) + (1-a) \cdot e(n-1)$ (3).

FQ5~489

The state state that the state state state state

·*- [

15

10

26

where a is a parameter (0 < a \le 1) and the initial value e(0) is equal to e(1). In other words, the current evaluated value e(n) is the sum of the current measured value m(n) multiplied by parameter a and the previous estimated value e(n-1) multiplied by (1-a). The expression (3) can be expanded as follows:

$$e(n) = a \cdot m(n) + (1-a) \cdot m(n-1) + a(1-a)^2 \cdot m(n-2) + a(1-a)^3 \cdot m(n-3) + \dots$$

This expanded expression shows that the estimated value e(n) includes all past measured values m(n), m(n-1), — such that more importance is given to a more recent measured value. That is, the larger the parameter a (nearer to 1), the more important a later measured value. If the measurement interval is enough short, then the reliability of an estimated value increases by making the parameter a smaller (for example, a = 0.1).

In the R method, Pei is calculated from Rei determined by the expression (3) by the following formula, and stored in the row T4.

Pei =
$$Rei/(1.0^{'} - Rei)$$
.

Pei = Nei when Rei \geq 0.99.

This expression is nothing but the relation between a system length and utilization in M/M/1 queue.

1.4 Selection of execution computer

The Grand Will will be set the first than the first than the section of the secti

10

20

FQ5-489

Referring to Fig. 4A, when a processing request message has arrived at the load balancing device 2 from a terminal (step 71), the execution computer selector 7 is started up by the communication section 21 and receives the processing request message from the communication section 21. Then, the execution computer selector 7 determines the estimated elongation rate of each computer from the load data of the computer stored in the load data memory 6 (steps 72 and 73) and calculates the value of a load index for each computer (step 74). Based on the load indexes of the computers, the execution computer selector 7 selects an execution computer with the smallest load index from the computers (step 75), and instructs the communication section 21 to send the processing request message to the selected computer (step 76).

27

1.4.1 Correction of estimated value

In the estimated value correction step 72 of Fig. 4A, the correction is made based on data stored in the load data memory 6. More specifically, the corrected number of inprocess job processing processes, Nri, and the corrected number of job processing processes existing in the CPU system, Pri, as current load data of a computer 1, are calculated by the following formulas:

 $Nri = w \cdot Npi + (1-w) \cdot Nei$ (4),

where w is a weighing coefficient ($w \le 1.0$) and is preferably about 0.8; and

25

FQ5-489

28

Pri = Pei + (Nri-Nei), when Nri ≧ Nei = Pei + (Nri/Nei), when Nri < Nei (5).

The expression (5) corrects Pei estimated from the past sampling data using the relation between Nei estimated under the same conditions as this and Nri that is the most reliable corrected value representing the actual state. In this manner, a corrected value near the current true value can be obtained from the latest estimated value, based on the estimation that is carried out only at certain time intervals.

10 1.4.2 Calculation of estimated elongation rate

In the estimated elongation rate calculation step 73. the estimated elongation rate Epi is determined from the next formula:

Epi =
$$X/(X \sim Pri \cdot Pri)$$
, when Pri $< Nri$,
= $Nri + 1.0$, when $Pri \ge Nri$ (6)
where $X = Nri \cdot (Pri+1)$.

The estimated elongation rate Epi is stored in the column T5 of the table as shown in Fig. 3.

The expression 6 is deduced from the relation
20 established concerning the mean value of equilibrium states
in one computer (one CPU) as shown in Fig. 2. The details
will be described hereafter.

It is assumed that transaction processing request messages arrive at a time interval following an exponential distribution, that is, according to a Poisson process. In

15

5

FQ5-489 29

addition, it is assumed that no queue will occur for disk device, that is, an infinite number of disk devices exist. Here, the number of in-process job processing processes, the number of job processing processes in the CPU system among them, and the CPU utilization are also assumed to be mean values, and all variables including them are shown with computer numbers and suffixes indicating estimated states omitted. For example, the number of in-process job processing processes is denoted by N, the number of job processing processes existing in the CPU system by P, the CPU utilization by R, and so on. Moreover, four kinds of symbols defined below are denoted with mean time per transaction:

F: processing time;

t: net processing time;

s: CPU use time; and

d: input/output time (t = s + t).

Further, the mean number of input/output being executed at the request from the object computer are denoted by D (N = P + D).

20 Referring to an expression (8.3) described in page 228 of the Prior Art 4, concerning mean values in equilibrium states,

$$F = s(P+1) + d = sP + t$$
 (6.1)

and since there is no queue for input/output,

$$d/s = D/R \tag{6.2}$$

FQ5-489

In addition, as known from the comparison of the expression (8.3) and an expression (8.1) described in page 228 of the Prior Art 4,

30

$$P + 1 = 1/(1-R)$$
,

5 therefore,

$$R = P/(1+P)$$
 (6.3).

Substituting the expression (6.3) into the expression (6.2), using d = t - s and N = P + D,

$$s = Pt/(P+D+PD) = Pt/(N(P+1) - P \cdot P)$$
 (6.4)

10 Further, substituting the expression (6.4) into the expression (6.1),

$$F = P \cdot Pt/(N(P+1) - P \cdot P) + t$$

= $N(P+1)t/(N(P+1) - P \cdot P)$
= $Xt/(X - P \cdot P)$

15 wherein, X = N(P+1).

Therefore, the expression (6) of elongation rate E is deduced as follows:

$$E = F/t = X/(X - P \cdot P).$$

The elongation rate E may also be represented using N and the CPU utilization 'R as follows:

$$E = N(1-R)/(N(1-R) - R \cdot R)$$
 (6.5).

The expression (6.5) can be obtained by applying the expression (6.3) representing the relation of R and P to the expression (6), or by using the expression (6.2) and the expression (6.3) starting from the expression (8.1)

25

5

FQ5-489

31

(representing the processing time by using R) described in the Prior Art 4 (the description of deduction is omitted). However, as for R, since it seems that there is no correction means corresponding to the one performed by the expression (5), the present embodiment converts R into P and corrects the converted P. Even when the CPU utilization is measured in this embodiment, the expression (6) using N and P is used as the definitive elongation rate expression.

Here, the expression (6) used to determine an estimated elongation rate in this embodiment is established in the case of the statistic equilibrium state of a system as described above, and if the equilibrium state continues for a certain time, the elongation rate can be estimated knowing the mean values of P and N during the time of the equilibrium state. Nei and Pei obtained from the expression (3) based on the history are considered to be used as the equilibrium mean values representing the actual state. However, as for N, an exact current value Npi is known. To reflect this in the position of dynamic load balancing, so in the aforementioned expression (4), the corrected value of N is obtained based on this principle.

1.4.3 Calculation of predicted elongation rate

Depending on a load index to be adopted, in addition to the actual estimated elongation rate Epi, a predicted elongation rate Eni is required in the case where an arrival

[_j

5

FQ5-489

32

message is scheduled for a computer i. There may be required only Eni. As in the case of Epi, Eni is also calculated in the step 73 of Fig. 4A.

If a predicted elongation rate Eni is required and the job characteristics of processing of an arrival message is not used, then Eni is calculated same as the expression (6), assuming that the number of transactions Nni after scheduling is Nri+w and the number of processes staying at the CPU is Pri+w. The calculated Eni is stored in the column T6 of the table as shown in Fig. 3.

If job characteristics of processing of an arrival message can be estimated and is used, then the proportion C of CPU time to its net processing time (sum of time actually using resources such as CPU and file devices, in other words, processing time without resource conflict) is estimated depending on the kind of message or others. The Pni is estimated using the estimated proportion C according to the following calculation steps.

Assuming that C_1 is an estimated value of C of the job mix on the computer i before scheduling, the estimated value C_1 corresponds to s/t in the expression (6.4), applying the corrected values of N and P to the expression (6.4),

C₁ = Pri/(Nri·(1+Pri) - Pri·Pir)

Assuming that C_2 is an estimated value of C of the new 25 job mix in the case where an arrival message of $C=C_0$ is

|= b

-, :

ļ= k

g,

120 3

[] [] 15

scheduled, Nri jobs having a mean value of C1 exist, and one job having a mean value of C_0 is added thereto and the sum becomes Nni. Therefore, their mean becomes,

$$C_2 = (Nri \cdot C_1 + C_0)/Nni.$$

5 From the expression (6.4), since $s/t = P/(NP + N - P \cdot P)$, applying this expression to the state after scheduling, and substituting Pni by y, because of $s/t = C_2$, the following relation is obtained:

$$C_2(Nni \cdot y + Nni - y^2) = y.$$

Arranging this relation, the following quadratic equation is obtained:

$$C_2y^2 + (1 - C_2 \cdot Nni)y - C_2 \cdot Nni = 0$$
 (7).

By solving the equation (7) for y, an estimated value of Pni after scheduling can be obtained. Since the constant term is negative, both a positive solution and a negative solution are obtained. The positive solution is adopted as Then, Eni is calculated similarly to the expression (6) (referred to as Eki, hereinafter).

1.4.4 Elongation rate

20 The elongation rate of a processing time is defined as a ratio of the response time of a job processing process (a processing time including a queuing time) to the net processing time. An elongation rate Ei is a job processing process elongation rate at a computer i. In the case of all 25 the computers providing the same processing speed, a

25

5

FQ5-489

34

processing process can be completed faster by a computer having a lower elongation rate and, therefore, the response time thereof can be reduced. An estimated elongation rate reflects program characteristics as group in operation (including not only the CPU use characteristic but also CPU-I/O use characteristic) in a job mix that is a group of processes in operation on the concerned computer. Moreover, there is a feature that the expression (6) is allowed to obtain an estimated elongation rate from only measurable data in operation without characteristics of individual jobs in operation. Although such a concept basically belongs to that of the Prior Art 4 and the expression 6 can be obtained by extending and transforming the expression 2, the present system can reflect the actual system state (job mix characteristics) and, moreover, improves the accuracy and dynamics by taking into consideration the total processing time (response time) including not only the time in the CPU system but also in the input/output devices. However, since the expression (6) is established based on a theory concerning balanced mean values, it cannot be said that a short-term state is estimated with 100% reliability.

1.4.5 Calculation of load index

At the step 74 of Fig. 4A, the value of load index for each computer is calculated using the load data stored in T2 to T6.

FQ5-489

35

As shown in Fig. 5, eight kinds of load index, Lp1, Lp2, La1-La3, and Lk1-Lk3, can be conceived. In an actual system, one kind may be chosen among them. The value of load index is obtained using a corresponding expression described in a table as shown in Fig. 5, and the result is stored as estimated load in the column T7 of the table of Fig. 3. A computer with a smaller load index is more preferable as a destination computer.

For the possible timing of load to be considered, there are timings before scheduling and after scheduling of an arrival message. A load index to be used before scheduling is denoted by "Lp". If it is after-scheduling timing, two cases are considered depending on whether the job characteristics of the arrival message are unknown (a load index is denoted by "La") or are able to be estimated (a load index is denoted by "Lk").

For each of these three cases, the elongation rate itself can be taken as load index (denoted as "Lx1"), or the estimated elongation rate multiplied by the number of inprocess job processing processes Nri/Nni can also be taken as load index (denoted as "Lx2"), wherein x is p, a, or k. The latter denoted as "Lx2" means total sum of estimated elongation rates of individual transactions on the computer i.

Further, from the viewpoint of load increase due to scheduling, it is possible to take an increment of the total

=1

10

and the

15 15

Ţ

"Lx3"). The selection of smallest increment of elongation rate leads to the selection of smallest increase of the system-wide total sum of response times due to the scheduling, resulting in that the minimization of overall mean response time can be expected. If the job characteristics of an arrival message can be estimated with a good considerable precision, then the adoption of Lk3, based on theory, allows expecting the best results. If good precision of job characteristics estimation can not be expected, the selection of Lk3 is not preferable, and it is better to adopt La2 from the point of smallest mean response time.

The correction of estimated value (step 72), the calculation of estimated elongation rate (step 73) and the load index calculation (step 74) are repeatedly performed for all computers that can process the arrival message. The estimated load indexes obtained by repeating the steps 72-74 are stored in the column T7 of the table as shown in Fig. 3.

1.4.6 Decision of destination computer

At the step 75 in Fig. 4A, the column T7 of the table as shown in Fig. 3 storing the estimated load indexes of the computers is searched for the smallest estimated load. A computer with the smallest estimated load is selected as the destination computer to be put in charge of processing the arrival message. Then, the execution computer selector 7

tion for the

14. Indi

15 15

25

[10

instructs the communication section 21 to send the arrival message to the selected computer i, which is prompted to start the processing.

1.5 Advantages

The first embodiment as described above provides the centralized load balancing control. More specifically, according to the first embodiment, the load balancing device 2 manages the load data of all the computers in real time and receives all processing request messages directly from terminals. When receiving a processing request message, the load balancing device 2 calculates the elongation rates and load indexes of the computers based on the previously received load data thereof and selects a computer with the smallest load index value as a destination computer to request the selected computer to process the processing request message. In this manner, the centralized load balancing control can be implemented, resulting in improved load balancing with reduced overhead.

SECOND EMBODIMENT

Now, a second embodiment of the present invention will be described in detail.

2.1 System configuration

Referring to Fig. 6, the second embodiment of the present invention is different, as for the composition, from the first embodiment in that the terminal devices of the

5

11 10

Į.

`*<u>.</u>]

- h

15

20

25

38

terminal group 5 are connected directly to the computers 1.1 through 1.n via the communication network 51 without the load balancing device 2 and that a switch and storage fabric 10 is connected to the computers 1.1 through 1.n.

Since the load balancing device 2 is removed, a computer 1.i includes a load data memory 6.i, an execution computer selector 7.i and load data measurement section 8.i in addition to the load data measurement section 1.i1. the transaction processing section 1.i2 and the communication section 1.i3. These respective sections may be realized by running respective ones of programs stored in a recoding media (not shown) as in the first embodiment.

A single transaction is processed as described below. A transaction processing request message addressed to a certain computer is sent from a terminal device belonging to the leminal group 5, and is received by the computer via the communication network 51. The computer, when receiving the transaction processing request message, determines whether to process that message by itself or to transfer it to another computer. When determined to transfer it to another computer, it is further determined which one of the other computers is to be selected as a destination computer. When the destination computer is determined, the arrival transaction processing request message is transferred to the destination computer through the switch and storage fabric 10. The

5

4) 10

'nį.

is lask

15 15

20

<u>.</u>

39

destination computer to be put in charge of processing the message executes the transaction processing of the request, produces a response message, and sends it back to the request originating terminal.

More specifically, the load data measurement section

1.i1 on the computer 1.i periodically measures the load of
the computer 1.i and the measured load data is processed by
the load data measurement section 8.i. The processed load
data is stored as estimated data in the load data memory 6.i,
and at the same time, sends the estimated data to all other
computers through the switch and storage fabric 10. Moreover,
the computers exchange notifications of the start and
termination of transaction processing with each other via the
switch and storage fabric 10. In this manner, the latest
load data of all computers are stored in the load data memory
6.i of each computer.

The execution computer selector 7.i receives a processing request message from a terminal and calculates the estimated elongation rate of each computer using estimated load data stored in the load data memory 6.i. Based on the estimated elongation rates of the computers, as described before, the execution computer selector 7.i determines an execution computer to be put in charge of processing the processing request message received from the terminal.

=== £. 222 32 **[] 15** II. <u>_</u>

[]

5

If it is determined that the computer i is put in charge of processing the processing request message, then the processing request message is transferred to the transaction processing section 1.i2. If it is determined that another computer x is put in charge of processing the processing request message, then the processing request message is sent to the destination computer x via the switch and storage fabric 10. When the computer x has received the processing request message, the transaction processing section 1.x2 allocates the job processing process under its own control to the received processing request message, and performs the processing. The job processing process repeatedly uses the CPU and accesses the files on the file device group 4 for program execution, produces a response message when the processing is terminated, and sends it to the request originating terminal through the communication network 51.

2.2 Operation

Next, an operation of the second embodiment will be described in detail.

20 In Fig. 6, the operation of the transaction processing section 1.i2 is the same as that of the first embodiment except that a processing request message is received directly from a terminal or from the other computer. Load data to be managed is also the same as that of the first embodiment as 25 shown in Fig. 3, and the load data for all computers are

5

l= k

4

in i

[] [] 15

Į.

C.

20

41

retained in the load data memory 6.i on each computer i. The load data measurement section 1.il on the computer 1.i measures periodically, as load data of the computer 1.i. the number of job processes, Pi, in the CPU system or the CPU utilization Ri, and the number of in-process job processing processes. Ni, at that moment. Then the load data measurement section 8.i processes them, determines Pei or Rei and Nei as estimated data, calculates Pei from Rei in the case of the R method, and stores Nei in the column T3 and Pei in the column T4 for a computer number i. At the same time, values of Nei and Pei are sent to all other computers via the switch and storage fabric 10 and the contents of the load data memory 6.x are updated. The calculation method of estimated data is identical to that of the first embodiment.

Besides, the load data measurement section 8.i updates the actual number of in-process transactions Npi on the computer 1.i (stored in column T2 in the table of Fig. 3) upon the reception of notification of transaction processing start and transaction processing termination on the computer 1.i from the transaction processing section 1i2 and, at the same time, sends it to all other computers through the switch and storage fabric 10 so as to make all the other computers update the contents of the load data memory 6.x.

2.3 Selection of execution computer

20

25

5

Referring to Fig. 4B, the execution computer selector 7.i is started on the computer 1.1 at which a processing request message arrives from a terminal (step 71). The execution computer selector 7.i calculates an estimated elongation rate of each computer from the load data of the computers stored in the load data memory 6.i (steps 72 and 73) and then calculates the value of load index (step 74) as described before.

Thereafter, the execution computer selector 7.i determines based on the load indexes whether the arrival message is to be processed by its own computer 1.i (step 751). If it is determined that it is not to be processed by its own computer (NO at step 751), then a destination computer with smallest load index is selected as an execution computer (step 75), and then the message is sent to the destination computer through the switch and storage fabric 10 so as to make the destination computer perform the processing (step 76). If it is determined that it is to be processed by its own computer (YES at step 751), then the execution computer selector 7.i selects itself (step 752) and performs the processing (step 76).

The correction of estimated value (step 72), the calculation of estimated elongation rate (step 73) and the load index calculation (step 74) are repeatedly performed for all computers that are able to process the arrived message.

EN PER

`. j

20

25

5) 10

43

The estimated load indexes obtained by repeating the steps 72-74 are stored in the column T7 of the table as shown in Fig. 3.

The execution computer selector 7.x according to the second embodiment is different from the first embodiment in the decision step 751 and the self-selection step 752. At the decision step 751, it is determined whether an actual estimated elongation rate Epi of its own computer is equal to or lower than a predetermined threshold (preferably it is relatively small of the order of 1.3). If it is equal to or lower than the predetermined threshold, then it is determined that the arrival message is processed by its own computer.

Otherwise, when the estimated load of its own computer is not the smallest one among the computers, in other words, another computer with smallest estimated load exists in the column T7 of the table stored in the load data memory 6.i, if the difference between the estimated load of its own computer and the smallest estimated load of the other computer is sufficiently small, then it is determined that the arrival message is processed by its own computer. The judgment of difference magnitude depends on the predetermined threshold.

In the case where any of six kinds other than the increment of total sum of elongation rates is adopted as load index, it is preferable that the difference judgment relies on the multiple threshold, preferably, 1.3 to about 1.5. For

5

The state of the s

- h

'n.

]] 15

20

44

example, if Epi of its own computer is equal to or lower than 1.3 times Epj of a computer j with smallest Ep, then it is determined that the arrival message is processed by its own computer.

In the case where the increment of total sum of elongation rates is adopted as load index, it is preferable to judge whether the difference of load indexes between two computers divided by the number of all transactions in the system exceeds the predetermined threshold. In other words, the judgment depends on the magnitude of increment of mean elongation rate of all transactions in execution. In this case, the threshold is preferably about 0.02. Here, the threshold is preferably high if the load measurement interval is long. The reason is that it is preferable to select a safe side where transaction transfers are less frequent because the estimated value is less reliable if the measurement interval is long.

In this embodiment, the processing execution by the origin computer at which the message arrived from the terminal is privileged because the transfer to another computer charges overhead to both the source and destination computers, and brings delay to the processing time of the object transaction itself. If the decision step using the predetermined threshold is removed, then almost all arrival

= h

<u>|</u>= <u>L</u>

Ji

20

25

transaction processing messages are most likely to be transferred to another computer.

If it is determined that the arrival message is processed by its own computer (YES at step 751), the arrival message is transferred to the transaction processing section 1.i2, so that the message is processed. If it is determined that the arrival message is not processed by its own computer (NO at step 751), the column T7 of the table as shown in Fig. 3 storing the estimated load indexes of the computers is searched for the smallest estimated load. A computer j with the smallest estimated load is selected as the destination computer to be put in charge of processing the arrival message. Then, the execution computer selector 7.i instructs the communication section 1.i3 to send the arrival message to the selected computer j to prompt the transaction processing section 1.j2 of the selected computer j to prompt to start the processing.

As described above, the load data memory 6.x of the computers 1.x has the identical contents, and each of the computers measures and calculates load data concerning itself and then sends its own load data to all other computers through the switch and storage fabric 10. However, if the switch and storage fabric 10 is provided with a sufficient capacity storage device as fast as main memory, then a part of the load data stored in the load data memory 6.x may also

5

4

- L

4

|= b

Ţi

10

46

be stored and held on the switch and storage fabric 10 to allow the load data to be shared by the whole system. From the viewpoint of load data updating overhead, this composition is more preferable.

For example, the data stored in the columns T1 to T4 of the table as shown in Fig. 3 are held by the switch and storage fabric 10, and each computer may calculate estimated elongation rate, estimated load, or the like based on data extracted from the switch and storage fabric 10, and decide an execution computer to execute the processing.

Moreover, if it is determined that the arrival message is processed by another computer, it may also be so composed to store the arrival message in the switch and storage fabric and send only a request notification to the destination computer in place of sending the message itself directly. In this case, the destination computer can take the message itself out from the switch and storage fabric 10 at its convenience asynchronous to the request notification.

2.4 Advantages

According to the second embodiment, there is no need of the load balancing device 2 as shown in Fig. 1. Therefore, the whole system can be composed at low cost. Although load balancing according to the centralized control is impossible, a relatively high performance in load balancing can be achieved under the distributed control by calculating the

Maria of the state of the state

| m | h

'4. j

<u>|</u>= 1:

131

20

25

10

elongation rate at a computer at which a processing request arrived from a terminal based on the load state data obtained by this computer and the other computers, selecting an execution computer at that moment considering also the transfer overhead, and executing the processing of the arrival message on the selected computer.

Graphs as shown in Figs. 8 and 9 are simulation results of the effect of load balancing under the distributed control according to the second embodiment. Here, assuming two kinds of transaction, one whose proportion of CPU time to the net processing time (450 milliseconds) is 5% on average and the other 60% on average, the arrival proportion between 5%-CPU-time transactions and 60%-CPU-time transactions is 7:3. It is further assumed that eight computers are provided, at which transactions randomly arrive, resulting in an equal arrival rate on average at each computer.

The horizontal axis of Fig. 8 indicates a load rate in proportion to the arrival rate and the vertical axis thereof indicates a mean response time (millisecond). The horizontal axis of Fig. 9 indicates a load rate in proportion to the arrival rate and the vertical axis thereof indicates variations in response times (standard deviation).

Respective curbs in Figs. 8 and 9 correspond to load balancing methods, in which ones shown by solid lines are associated with the second embodiment and other ones shown by

15

20

FQ5-489

48

dotted lines with a third embodiment as described later. method processes arrival transactions without load balancing, MPL method is dynamic load balancing control taking the actual number of in-process transactions as a load index. the MPL method, if there is a computer having a load index smaller than that of the arrival computer by 2 or more, then the arrival transaction is transferred to the computer having the smallest load index, in which the transaction is processed. La2 and Lk3 correspond to the dynamic load balancing control using the load index based on the estimated elongation rate according to the second embodiment. These results teach that, concerning the mean response time, the dynamic control is significantly better than the NC method which is optimal for static stochastic load balancing and particularly show that the method based on the estimated elongation rate is more excellent than methods based on the number of in-process transactions used in the prior art, and further teach that the similar trend is more remarkably in variations in the response time. Such an advantage of the second embodiment is brought to the fore when the load rate is high.

THIRD EMBODIMENT

A third embodiment of the present invention will be described in detail with reference to Fig. 7-9.

25 3.1 System configuration

Mar. Mar.

Fig. The ACP CLA

<u>|</u>= 1

`+ <u>i</u>

ļ_k

20

25

10

As shown in Fig. 7, the third embodiment of the present invention is different in system configuration from the second embodiment. In the third embodiment, an interim load balancing device 25 is provided, through which the terminal group 5 is connected to each of the computers 1.1 - 1.n.

A single transaction is processed as described below. A transaction processing request message is sent from a terminal device belonging to the terminal group 5. and is received by the interim load balancing device 25 via the communication network 51. The interim load balancing device 25, when receiving the transaction processing request message, determines an interim destination computer according to static/quasi-static decision scheme or without using dynamic information in the dynamic load balancing control. The details of the static/quasi-static decision scheme will be described later.

When the interim destination computer is determined, the interim load balancing device 25 sends the arrival message to the interim destination computer. When receiving the message from the interim load balancing device 25, the computer determines whether to process that message by itself or to transfer it to another computer. When determined to transfer it to another computer, it is further determined which one of the other computers is to be selected as a final destination computer. When the final destination computer is determined,

g1

100 to

٠<u>٠</u> ۽

15

10 11 11 50

the arrival transaction processing request message is transferred to the final destination computer through the switch and storage fabric 10. The final destination computer to be put in charge of processing the message executes the transaction processing of the request, produces a response message, and sends it back to the request originating terminal through the interim load balancing device 25.

3.2 Interim load balancing

Similarly to the load balancing device 2 of the first embodiment (see Fig. 1), the interim load balancing device 25 receives all processing request messages from the terminal group 5, determines an interim destination computer according to the static/quasi-static interim load balancing scheme, and sends an arrival message to the interim destination computer. Three kinds of static/quasi-static interim load balancing scheme are supposed as follows:

- Distribution based on terminal grouping;
- 2) Round distribution in the arrival order; and
- 3) Stochastic distribution based on past data.

Among the three methods, one or more is selected or combined. Other load balancing methods achieving a reduced collection of load data of computers and a low execution overhead may be adopted.

(1) Distribution based on terminal grouping:

10

FQ5-489

51

In the distribution based on terminal grouping, an interim destination computer is fixedly determined depending on which group a message-originating terminal belongs to. More specifically, the terminal group 5 is previously divided into a plurality of subgroups each associated with the computers 1.1-1.n. For example, a message originating from a first subgroup is processed by the computer 1.1, a message originating from a second subgroup is processed by the computer 1.2, and so on. In this way, an interim destination is uniquely decided according to a message-originating subgroup. Although this method is almost same as the second embodiment, this method can intensively manage a correspondence between the terminal subgroups and the computers at the interim load balancing device 25 and therefore it is easy to revise the grouping for better load balance in the long term, for example, at each system startup, based on past data.

(2) Round distribution in the arrival order:

In the round distribution in the arrival order, for 20 example, the message arriving first at the interim load balancing device 25 is distributed to the computer 1.1, and the next to the computer 1.2, and so on. A message following the message distributed to the last computer 1.n is d1str1buted again to the first computer 1.1, thus going round. 25 Particularly, in the case where job characteristics of most

10

20

FQ5-489

52

of messages belong to the same class, the load balancing effect can be also expected even in the short term.

(3) Stochastic distribution based on past data:

In the stochastic distribution based on past data, the proportion of the number of messages to be distributed is set for each computer, and this distribution proportion is kept even in the short term. Load data is received periodically from each computer, such as every 1 sec or 10 sec, and if loads are imbalanced, then the distribution proportion to individual computers is increased/reduced to balance, and thereafter, the distribution is performed based on this updated distribution proportion.

The operations of the transaction processing section 1.x2 and the contents of the load data memory 6.x on the computer 1.x as shown in Fig. 7 are the same as those in the second embodiment. The load data measurement section 1.x1 and the load data measurement section 8.x on the computer 1.x also operate as in the case of the second embodiment. However, in addition to that, the load data measurement section 8.x sends the summary of load data to the interim load balancing device 25 at intervals of 1 sec, 10 sec or others, in the case where the interim load balancing device 25 adopts the aforementioned distribution method (3). The operation of the execution computer selector 7.x of the third

5

FQ5-489

53

embodiment is the same as in the second embodiment, and is illustrated by the flow chart of Fig. 4B.

As described above, the load data memory 6.x of the computers 1.x has the identical contents, and each of the computers measures and calculates load data concerning itself and then sends its own load data to all other computers through the switch and storage fabric 10. However, if the switch and storage fabric 10 is provided with a sufficient capacity storage device as fast as main memory, then a part of the load data stored in the load data memory 6.x may also be stored and held on the switch and storage fabric 10 to allow the load data to be shared by the system. From the viewpoint of load data updating overhead, this composition is more preferable.

Moreover, if it is determined that the arrival message is processed by another computer, it may also be so composed to store the arrival message in the switch and storage fabric and send only a request notification to the destination computer in place of sending the message itself directly. In this case, the destination computer can take the message itself out from the switch and storage fabric 10 at its convenience asynchronous to the request notification.

3.3 Advantages

According to the third embodiment, the interim load balancing device 25 has a relatively simple function as

5

E I

10 11

|== l=

, F.

in i

[] [] 15

20

54

described above. Therefore, the amount of data to be collected from the computers and the data collection frequency can be reduced, resulting in improved utilization efficiency of the network and further reduction in cost.

Furthermore, the interim destination computer is determined and then the arrival message is transferred to the interim destination computer. Accordingly, the response performance such as mean and variations can be improved and the message transfer frequency from the interim destination computer to a final destination computer can be substantially reduced, compared with the case where the interim load balancing device 25 is not provided.

As shown in Figs. 8 and 9, respective curbs shown by dotted lines are associated with the third embodiment. In R_NC method, the round distribution in the arrival order (2) is performed as interim load balancing and thereafter the interim destination computer processes arrival transactions without load balancing. In R_Lk3 method, the round distribution in the arrival order (2) is performed as interim load balancing and thereafter the interim destination computer performs the dynamic load balancing control taking Lk3 in Fig. 5 the actual number of in-process transactions as a load index.

These results teach that the quasi-static load balancing control significantly improve the response performance,

41

]] 10

in h

15

20

25

Ţ

55

compared with the static load balancing NC method and particularly show that a combination of the quasi-static load balancing method and the method using the estimated elongation rate further improves the response performance, compared with the second embodiment.

ADVANTAGES OF INVENTION

According to the present invention, the dynamic distribution of processing requests is performed using a novel load index obtained from the elongation rate of computer processing time. In other words, the processing time elongation rate, as a basic performance index, reflects the program characteristics (including not only CPU characteristics but also CPU-I/O use characteristics) as a group of in-operation processes in a job mix. Therefore, as basic performance idex, the processing time elongation rate is most suitable for interactive processing and has a wide applicability. Accordingly, the improved load balancing among computers can be achieved even in the short term, and the mean and variation of response times can be kept small as the whole.

Moreover, according to the present invention, the load balancing can be realized based on the elongation rate of computer processing time, which can be obtained from only data observable during operation, such as the number of in-process transactions at each computer at certain time

See H. H. Carp. State House Brief. B. H.

ļ= i.

= h

[] [] 15

20

25

10

56

intervals, the number of job processing processes staying in the CPU system, and the CPU utilization. Therefore, the elongation time-based load balancing control can be achieved using only the observable load data with reduced overhead without apriori knowledge concerning job characteristics of individual processing requests.

Additionally, the load state of each computer is estimated using comprehensively a series of load data measured periodically. The current number of in-process transactions at each computer can be grasped at all times according to the transaction processing start/end notification of the computer. Since the estimated load data is corrected using this current number of in-process transactions, the estimation precision of estimated load can be increased. Consequently, the elongation rate estimation accuracy and the load balancing accuracy can further be improved.

Further, the load index of each computer can use the following data: the estimated elongation rate itself; the total estimated elongation rate before or after allocating a newly arrived transaction to a computer (the estimated elongation rate of processing time in the concerned computer multiplied by the number of in-process transactions at the concerned computer); or the difference between the total estimated elongation rate after allocating the arrival

57

transaction to the concerned computer and the total estimated elongation rate before the allocation. Accordingly, the load can be balanced based on a load index representing load level of individual computers or of the whole system, and not only the minimization of processing time of individual transactions themselves but also the optimization of whole the system taking into consideration the effect of that allocation on the others can be realized.

The present invention can be equally applicable to a system designed to distribute intensively and dynamically individual processing requests to the computers or to a system designed such that, after the static/quasi-static load balancing control, an interim destination computer can perform the processing by itself or transfer the processing request message to another computer with lighter load.

58

WHAT IS CLAMED IS:

- 1. A load balancing method in a system comprising a plurality of computers for processing transaction processing requests originating from a plurality of terminals, comprising steps of:
- 5 a) estimating load states of respective ones of the computers;
 - b) determining estimated elongation rates of processing time for respective ones of the computers based on the estimated load states;
 - c) calculating load indexes of respective ones of the computers from the estimated elongation rates; and
 - d) determining load distribution among the computers based on the load indexes.
- 2. The load balancing method according to claim 1, 15 wherein the step (a) comprises the steps of:
 - a.1) measuring load data of each of the computers at constant intervals; and
 - a.2) estimating a load state of each of the computers based on the load data.

15

20

FQ5-489

59

 The load balancing method according to claim 2, wherein,

in the step (a), a current number of in-process transactions in each of the computers is measured in response to start and termination of transaction processing at each of the computers, and

in the step (b), an estimated elongation rate of processing time for each of the computers is determined based on the estimated current load state and the current number of in-process transactions in the computer.

4. The load balancing method according to claim 1, wherein the steps (a) through (d) are sequentially performed when a transaction processing request has been received from a terminal,

wherein, in the step (d), one of the computers is selected based on the load indexes as an execution computer processing the transaction processing request.

5. The load balancing method according to claim 2, wherein. in the step (a.1), a number of in-process transactions and a number of job processing processes staying in a CPU system of the computer are measured as the load data measured at constant intervals.

FQ5-489

60

- The load balancing method according to claim 2. 6. wherein, in the step (a.1), a number of in-process transactions and a CPU utilization of the computer are measured as the load data measured at constant intervals.
- 5 The load balancing method according to claim 1, wherein the step (a) comprises the steps of:
 - a.1) measuring load data of each of the computers at constant intervals to produce a sequence of load data; and
 - a.2) estimating a current load state of each of the computers based on the sequence of load data.
 - 8. The load balancing method according to claim 3, wherein the step (a) comprises the steps of:
 - a.1) measuring load data of each of the computers at constant intervals to produce a sequence of load data; and a.2) estimating a current load state of each of the
 - 9. The load balancing method according to claim 3, wherein the step (b) comprises the steps of:

computers based on the sequence of load data.

b.1) correcting the estimated load states using the 20 current numbers of in-process transactions for respective ones of the computers to produce corrected estimated load states; and

is l= h

10

61

- b.2) determining estimated elongation rates of processing time for respective ones of the computers based on the corrected estimated current load states.
- 10. The load balancing method according to claim 8, wherein the step (b) comprises the steps of:
 - b.1) correcting the estimated load states using the current numbers of in-process transactions for respective ones of the computers to produce corrected estimated load states; and
 - b.2) determining estimated elongation rates of processing time for respective ones of the computers based on the corrected estimated current load states.
 - 11. The load balancing method according to claim 1, wherein, in the step (c), an estimated elongation rate is used as a load index of each of the computers.
 - 12. The load balancing method according to claim 1, wherein, in the step (c), an estimated elongation rate is one of a before-scheduling estimated elongation rate and an after-scheduling estimated elongation rate,
- wherein the before-scheduling estimated elongation rate is an estimated elongation rate calculated from a corresponding estimated load state before the transaction

FQ5-489

62

processing request is allocated to respective ones of the computers, and

the after-scheduling estimated elongation rate is an estimated elongation rate calculated from a corresponding estimated load state after the transaction processing request is allocated to respective ones of the computers.

- 13. The load balancing method according to claim 12.
 wherein the step (c) comprises the steps of:
- c.1) multiplying the estimated elongation rate of each of the computers by a current number of in-process transactions in the computer to produce a total estimated elongation rate of the computer; and
- c.2) determining the total estimated elongation rate as a load index of the computer.
- 14. The load balancing method according to claim 12, wherein the step (c) comprises the steps of:
- c.1) multiplying the estimated elongation rate of each of the computers by a current number of in-process transactions in the computer to produce a total estimated elongation rate of the computer;
- c.2) calculating a total estimated elongation rate difference between an after-scheduling total estimated

5

FQ5-489

63

elongation rate and a before-scheduling total estimated elongation rate of each of the computers; and

- c.3) determining the total estimated elongation rate difference as a load index of the computer.
 - 15. A load balancing system comprising:
- a plurality of terminals, each of which originates a transaction processing request:
- a plurality of computers, each of which processes a plurality of transaction processing requests originating from a plurality of terminals in parallel:
- a load estimator for estimating load states of respective ones of the computers;
- a load data memory for storing the estimated load states; and

an execution computer selector for selecting one of the computers as an execution computer to be put in charge of processing a transaction processing request based on load indexes of respective ones of the computers, wherein the load indexes are calculated from estimated elongation rates of processing time for respective ones of the computers, wherein the estimated elongation rates are determined based on the estimated load states.

The state of the s

= b

64

- 16. The load balancing system according to claim 15, wherein the load estimator measures load data of each of the computers at constant intervals and estimates a load state of each of the computers based on the load data.
- 17. The load balancing system according to claim 16, wherein the load estimator measures a current number of inprocess transactions in each of the computers in response to start and termination of transaction processing at each of the computers; and

the execution computer selector determines an estimated elongation rate of processing time for each of the computers based on the estimated current load state and the current number of in-process transactions in the computer.

- 18. The load balancing system according to claim 15, wherein the execution computer selector is started up when a transaction processing request has been received from a terminal and then selects one of the computers based on the load indexes as an execution computer processing the transaction processing request.
- 20 19. The load balancing system according to claim 16, wherein the load estimator measures a number of in-process transactions and a number of job processing processes staying

15

20

10

65

in a CPU system of the computer as the load data measured at constant intervals.

- 20. The load balancing system according to claim 16. wherein the load estimator measures a number of in-process transactions and a CPU utilization of the computer as the load data measured at constant intervals.
 - 21. The load balancing system according to claim 15, wherein the load estimator measures load data of each of the computers at constant intervals to produce a sequence of load data, and estimates a current load state of each of the computers based on the sequence of load data.
 - 22. The load balancing system according to claim 17, wherein the load estimator measures load data of each of the computers at constant intervals to produce a sequence of load data, and estimates a current load state of each of the computers based on the sequence of load data.
 - 23. The load balancing system according to claim 17, wherein the execution computer selector corrects the estimated load states using the current numbers of in-process transactions for respective ones of the computers to produce corrected estimated load states, and determines estimated

FQ5-489

66

elongation rates of processing time for respective ones of the computers based on the corrected estimated current load states.

- 24. The load balancing system according to claim 22, wherein the execution computer selector corrects the estimated load states using the current numbers of in-process transactions for respective ones of the computers to produce corrected estimated load states, and determines estimated elongation rates of processing time for respective ones of the computers based on the corrected estimated current load states.
 - 25. The load balancing system according to claim 15, wherein the execution computer selector uses an estimated elongation rate as a load index of each of the computers.
- 15 26. The load balancing system according to claim 15, wherein an estimated elongation rate is one of a beforescheduling estimated elongation rate and an after-scheduling estimated elongation rate,

wherein the before-scheduling estimated elongation

20 rate is an estimated elongation rate calculated from a

corresponding estimated load state before the transaction

FQ5-489

67

processing request is allocated to respective ones of the computers, and

the after-scheduling estimated elongation rate is an estimated elongation rate calculated from a corresponding estimated load state after the transaction processing request is allocated to respective ones of the computers.

- 27. The load balancing system according to claim 26, wherein the execution computer selector multiplies the estimated elongation rate of each of the computers by a current number of in-process transactions in the computer to produce a total estimated elongation rate of the computer, and determines the total estimated elongation rate as a load index of the computer.
- 28. The load balancing system according to claim 26, wherein the execution computer selector multiplies the estimated elongation rate of each of the computers by a current number of in-process transactions in the computer to produce a total estimated elongation rate of the computer, calculates a total estimated elongation rate difference between an after-scheduling total estimated elongation rate and a before-scheduling total estimated elongation rate of each of the computers, and determines the total estimated elongation rate difference as a load index of the computer.

Open Will had had been the

|=b 10

20

,= i

68

29. The load balancing system according to claim 15, further comprising:

a control node connected between the terminals and the computers, wherein the control node includes the load estimator, the load data memory, and the execution computer selector.

- 30. The load balancing system according to claim 15, further comprising:
- a switching device connecting the computers to each other,

wherein each of the computers includes the load estimator, the load data memory, and the execution computer selector, wherein an estimated load state estimated at a computer is transferred to all other computers through the switching device so that the load state memory of each of the computers stores same load state data.

31. The load balancing system according to claim 30, wherein the execution computer selector selects a computer to which the execution computer selector belongs when the load index of the computer is smaller than a value obtained by multiplying a minimum load index among the computers by a predetermined factor greater than 1.

20

FQ5-489

69

32. The load balancing system according to claim 15, further comprising:

a switching device connecting the computers to each other, the switching device including the load data memory which is shared among the computers.

wherein each of the computers includes the load estimator and the execution computer selector, wherein an estimated load state estimated at a computer is transferred to the switching device to be stored in the load state memory.

33. The load balancing system according to claim 15, further comprising:

an interim control node connected between the terminals and the computers, wherein the interim control node includes an interim execution computer selector for selecting one of the computers according to a predetermined rule; and

a switching device connecting the computers to each other,

wherein each of the computers includes the load estimator, the load data memory, and the execution computer selector, wherein an estimated load state estimated at a computer is transferred to all other computers through the switching device so that the load state memory of each of the computers stores same load state data, and

20

5

FQ5~489

70

wherein the execution computer selector selects a computer to which the execution computer selector belongs when the load index of the computer is smaller than a value obtained by multiplying a minimum load index among the computers by a predetermined factor greater than 1.

The load balancing system according to claim 15, further comprising:

a switching device connecting the computers to each other, the switching device including the load data memory which is shared among the computers,

wherein each of the computers includes the load estimator and the execution computer selector, wherein an estimated load state estimated at a computer is transferred to the switching device to be stored in the load state memory, and

wherein the execution computer selector selects a computer to which the execution computer selector belongs when the load index of the computer is smaller than a value obtained by multiplying a minimum load index among the computers by a predetermined factor greater than 1.

35. The load balancing method according to claim 1. wherein the estimated elongation rate E is a ratio of a processing time required for a job processing process to a

71

net processing time, wherein the estimated elongation rate E is obtained by the following equation:

$$E = X/(X - P \cdot P), \text{ when } P < N,$$
$$= N + 1.0, \text{ when } P \ge N,$$

- where $X = N^{*}(P + 1)$, N is the number of in-process transactions in a CPU of the computer and P is the number of transaction processes in a CPU system of the computer.
 - 36. The load balancing method according to claim 1. wherein the estimated elongation rate E is obtained by the following equation:

$$E = N(1-R)/(N(1-R)-R\cdot R)$$

where N is a number of in-process transactions in a CPU of the computer and R is a CPU utilization.

- 37. A load balancing method in a system comprising a plurality of computers for processing transaction processing requests originating from a plurality of terminals. comprising steps of:
- a) estimating elongation rates of processing time for respective ones of the computers, wherein an elongation rate is a ratio of a processing time required for processing a transaction to a net processing time which is a sum of CPU time and an input/output time for processing the transaction;

FQ5-489

- b) calculating load indexes of respective ones of the computers based on the estimated elongation rates; and
- c) selecting a destination computer from the computers based on the load indexes, wherein the destination computer having a minimum one among the load indexes.
- 38. A load balancing method in a system comprising a load balancing device for distributing transaction processing requests originating from a plurality of terminals to a plurality of execution computers, comprising steps of:
 - at the load balancing device,
- a) receiving load data from each of the execution computers at regular intervals, the load data including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the execution computer;
- b) estimating load states of respective ones of the computers based on load data received from the execution computers;
- c) determining estimated elongation rates of
 processing time for respective ones of the computers based on
 the estimated load states;
 - d) calculating load indexes of respective ones of the computers from the estimated elongation rates; and

FQ5-489

- e) determining load distribution among the computers based on the load indexes.
- 39. A load balancing method in a system comprising a plurality of computers and a plurality of terminals, wherein
 5 each of the computers processes a transaction processing requests originating from a terminal, comprising steps of:

73

at each of the computers,

- a) estimating a load state of the computer based on load data measured at regular intervals including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the computer;
- b) storing the load state of the computer and a load state received from another computer in a load state memory;
- c) transferring the load state of the computer to all other computers;
- d) when receiving a transaction processing request from a terminal, determining estimated elongation rates of processing time for respective ones of the computers based on the load states stored in the load state memory;
- e) calculating load indexes of respective ones of the computers from the estimated elongation rates;

FQ5-489

- f) determining based on the load indexes whether
 the transaction processing request should be processed by the
 computer or transferred to another computer;
- g) when it is determined that the transaction processing request should be transferred to another computer, determining a destination computer among the computers based on the load indexes to transfer it to the destination computer; and
 - h) when it is determined that the transaction processing request should be processed by the computer, processing the transaction processing request.
 - 40. A load balancing method in a system comprising an interim load balancing device connecting a plurality of terminals and a plurality of execution computers, comprising steps of:
 - at the interim load balancing device.
 - a) setting a predetermined distribution scheme;
 - b) when receiving a transaction processing request from a terminal, selecting an interim destination execution computer from the execution computers according to the predetermined distribution scheme;
 - c) sending the transaction processing request to the interim destination execution computer;
 - at each of the execution computers,

FQ5-489

- d) estimating a load state of the execution computer based on load data measured at regular intervals including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the execution computer;
- e) storing the load state of the execution computer and a load state received from another execution computer in a load state memory;
- f) transferring the load state of the execution computer to all other execution computers;
- g) when receiving the transaction processing request from the interim load balancing device, determining estimated elongation rates of processing time for respective ones of the execution computers based on the load states stored in the load state memory;
- h) calculating load indexes of respective ones of the execution computers from the estimated elongation rates;
- i) determining based on the load indexes whether the transaction processing request should be processed by the execution computer or transferred to another execution computer;
- j) when it is determined that the transaction processing request should be transferred to another execution computer, determining a final destination computer among the

FQ5-489 76

execution computers based on the load indexes to transfer it to the final destination computer; and

- k) when it is determined that the transaction processing request should be processed by the execution computer, processing the transaction processing request.
- 41. A load balancing method according to claim 40, wherein, in the step (a), the predetermined distribution scheme is a static distribution method such that the terminals are previously divided into a plurality of groups and an interim destination execution computer is determined depending on which one of the groups a request originating terminal belongs to.
- 42. A load balancing method according to claim 40, wherein, in the step (a), the predetermined distribution scheme is a static round distribution method such that the execution computers are sequentially and repeatedly selected as an interim destination execution computer in the arrival order.
- 43. A load balancing method according to claim 40, 20 wherein the step (a) comprises the steps of:
 - a.1) receiving load data from each of the execution computers at regular intervals, the load data including a

FQ5-489

77

number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the execution computer;

- a.2) determining the predetermined distribution scheme based on the load data so that a transaction processing load is balanced among the execution computers.
 - 44. A recording medium storing a computer program for instructing a computer of a load balancing device to distribute transaction processing requests originating from a plurality of terminals to a plurality of execution computers, the computer program comprising steps of:
 - a) receiving load data from each of the execution computers at regular intervals, the load data including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the execution computer;
 - b) estimating load states of respective ones of the computers based on load data received from the execution computers;
- c) determining estimated elongation rates of processing time for respective ones of the computers based on the estimated load states;
 - d) calculating load indexes of respective ones of the computers from the estimated elongation rates: and

IJ1

lue la

I) 15

20

- e) determining load distribution among the computers based on the load indexes.
- 45. A recording medium storing a computer program for instructing a computer to balance a transaction processing load among a plurality of computers, the computer program comprising steps of:
- a) estimating a load state of the computer based on load data measured at regular intervals including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the computer;
- b) storing the load state of the computer and a load state received from another computer in a load state memory;
- c) transferring the load state of the computer to all other computers;
- d) when receiving a transaction processing request from a terminal, determining estimated elongation rates of processing time for respective ones of the computers based on the load states stored in the load state memory;
- e) calculating load indexes of respective ones of the computers from the estimated elongation rates;

79

- f) determining based on the load indexes whether the transaction processing request should be processed by the computer or transferred to another computer;
- g) when it is determined that the transaction

 5 processing request should be transferred to another computer,
 determining a destination computer among the computers based
 on the load indexes to transfer it to the destination
 computer; and
 - h) when it is determined that the transaction processing request should be processed by the computer, processing the transaction processing request.

46. A recording medium storing:

a first computer program for instructing a first computer of an interim load balancing device to distribute transaction processing requests originating from a plurality of terminals to a plurality of execution computers; and

a second computer program for instructing each of the execution computers to balance a transaction processing load among the execution computers,

- wherein the first computer program comprises the steps of:
 - a) setting a predetermined distribution scheme;

FQ5-489

80

- b) when receiving a transaction processing request from a terminal, determining an interim execution computer according to the predetermined distribution scheme; and
- c) sending the transaction processing request to 5 the interim execution computer,

wherein the second computer program comprises the steps of:

- d) estimating a load state of the execution computer based on load data measured at regular intervals including a number of in-process transactions and one of a CPU utilization and a number of job processing processes staying in a CPU system of the execution computer:
- e) storing the load state of the execution computer and a load state received from another execution computer in a load state memory;
- f) transferring the load state of the execution computer to all other execution computers;
- g) when receiving the transaction processing request from the interim load balancing device, determining estimated elongation rates of processing time for respective ones of the execution computers based on the load states stored in the load state memory:
- h) calculating load indexes of respective ones of the execution computers from the estimated elongation rates;

81

- i) determining based on the load indexes whether the transaction processing request should be processed by the execution computer or transferred to another execution computer;
- j) when it is determined that the transaction processing request should be transferred to another execution computer, determining a final destination computer among the execution computers based on the load indexes to transfer it to the final destination computer; and
- k) when it is determined that the transaction processing request should be processed by the execution computer, processing the transaction processing request.

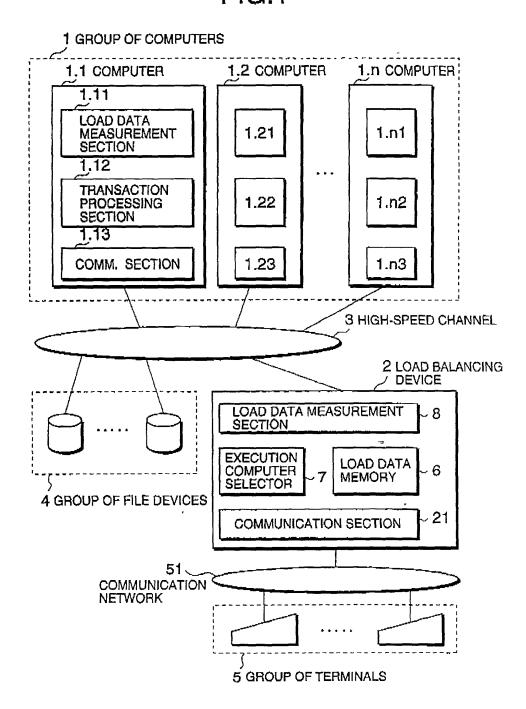
82

ABSTRACT OF THE DISCLOSURE

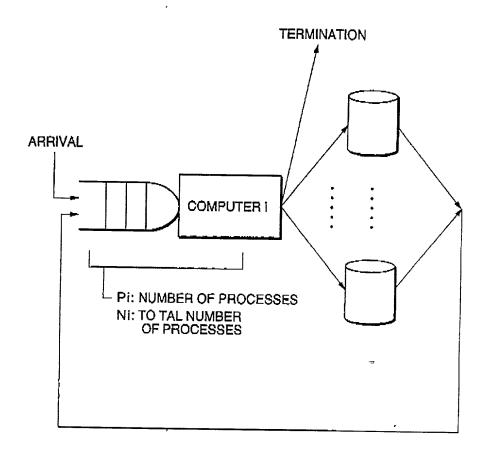
A dynamic load balancing method allowing a transaction processing load is balanced among computers by using estimated elongation rates is disclosed. In a system composed of a plurality of computers for processing transaction processing requests originating from a plurality of terminals, elongation rates of processing time for respective ones of the computers are estimated, where an elongation rate is a ratio of a processing time required for processing a transaction to a net processing time which is a sum of CPU time and an input/output time for processing the transaction. After calculating load indexes of respective ones of the computers based on the estimated elongation rates, a destination computer is selected from the computers based on the load indexes, wherein the destination computer having a minimum one among the load indexes.

10 The state of th

1/9 FIG.1



2/9 FIG.2



3/9

1							
ESTIMATED LOAD	Lxy1			Lxy1		Lxyn	
ESTIMATED ELONGATION RATE AFTER DISTRIBUTION	En 1			Ш i=		Enn	
ESTIMATED ELONGATION RATE	Ep1			Epi		Epn	
ESTIMATED NUMBER OF TRS/CPU- SYSTEM	Pe1			Pei		Pen	
ESTIMATED NUMBER OF TRS	Ne 1			Nei		Nen	
NUMBER OF CURRENT TRS	N p 1			Npi	• • •	, udN	
COMPUTER #	-	2				С	
	ESTIMATED NUMBER OF TRS/CPU- TRS SYSTEM ESTIMATED ESTIMATED ESTIMATED ELONGATION RATE AFTER DISTRIBUTION	NUMBER OF NUMBER OF TRS/CPU- RATE DISTRIBUTION RATE AFTER DISTRIBUTION RATE AF	NUMBER OF NUMBER OF TRS/CPU- STIMATED ESTIMATED ELONGATION RATE AFTER SYSTEM Np1 Ne1 Pe1 Ep1 En1	NUMBER OF NUMBER OF TRS/CPU- SYSTEM RATE AFTER DISTRIBUTION RATE AFTER DISTRIB	NUMBER OF TRS/CPU- STIMATED RATE AFTER INJUMBER OF TRS/CPU- SYSTEM RATE AFTER DISTRIBUTION RATE AFTER	NUMBER OF TRS/CPU- STIMATED RETIMATED RESTIMATED STIMATED FLONGATION RATE AFTER SYSTEM Np1 Ne1 Pe1 Ep1 En1 Npi Nei Pei Epi Eni Npi Nei Pei Epi Eni	NUMBER OF THE ESTIMATED RETIMATED RETIMATED LONGATION RATE ELONGATION RATE ELONGATION RATE DISTRIBUTION NP1 Nei Pei Epi Eni Eni Npi Nei Pei Epi Eni Eni

=p/a/K y=1/2/3



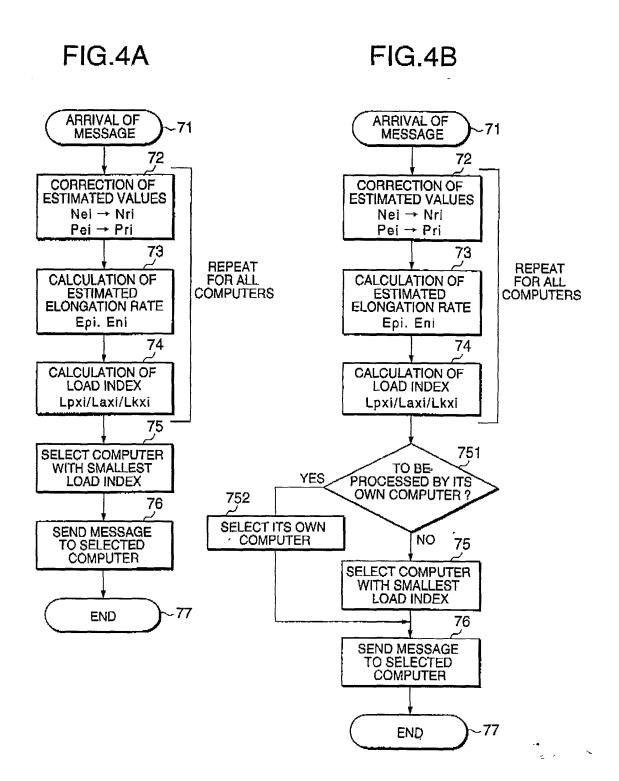
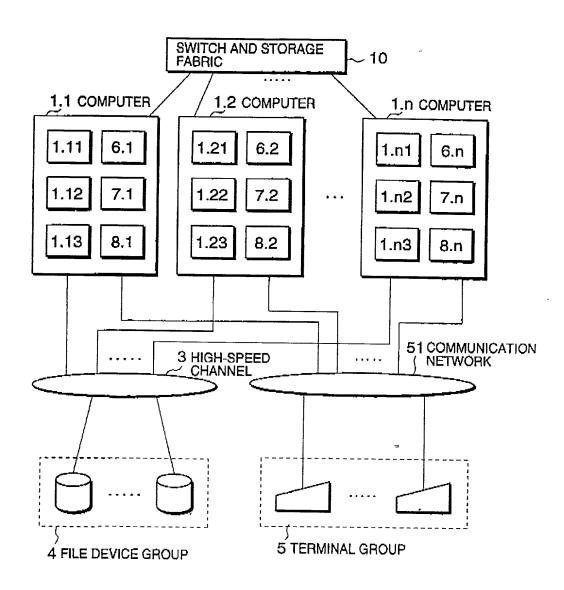


FIG.5

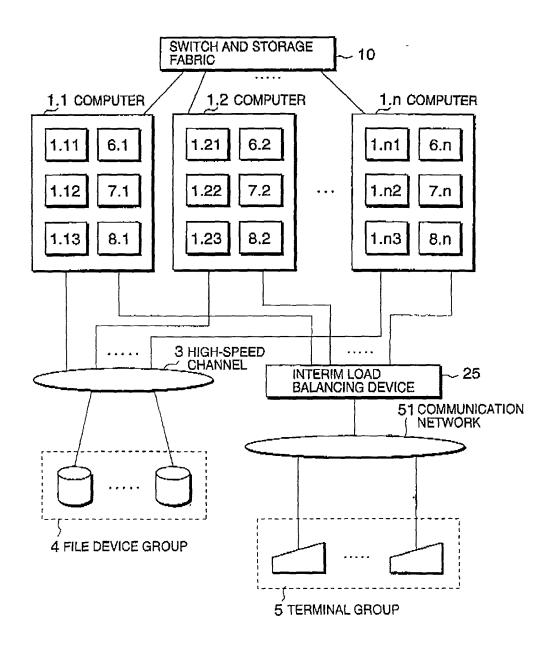
ELONGATION RATE	ESTIMATED	ESTIMATED ELONGATION RATE AFTER SCHEDULING	NGATION RATE HEDULING
LOAD INDEX	BEFORE SCHEDULING	UNKNOWN CHARACTERISTICS	ESTIMATED CHARACTERISTICS
ELONGATION RATE ASITIS	гр1≃Ері	La1=Eni	LK1=EKi
ELONGATION RATE MULTIPLIED BY NUMBER OF TRANSACTIONS	Lp2=Nri × Epi	La2=Nni × Eni	Lk2=Nni × Eki
AN INCREASE IN TOTAL ELONGATION RATE		La3=La2 – Lp2	Lk3=Lk2 - Lp2

6∕9 FIG.6

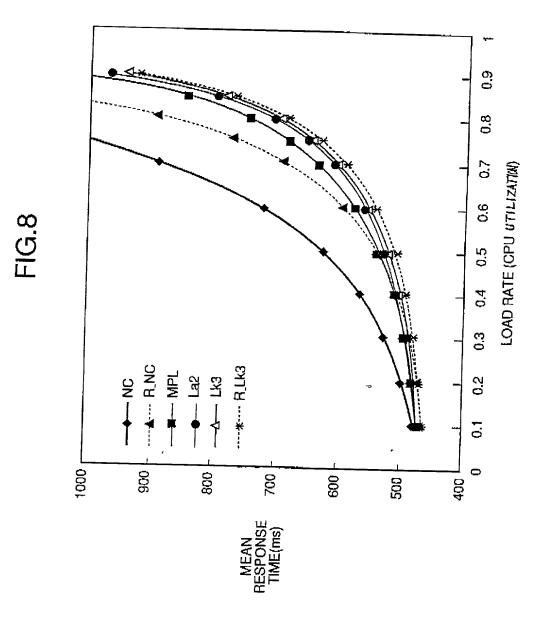


FQ5-489

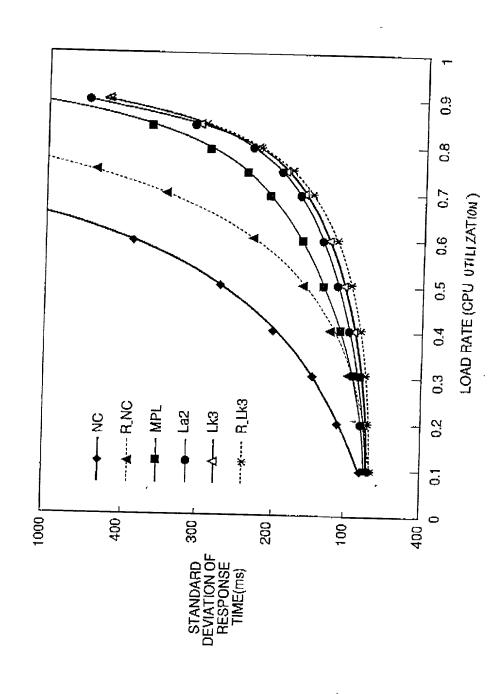
7/9 **FIG.7**



8/9







Docket No			
11-2852:1	FOE	65/	(A:2)

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:							
My residence, post office address and	My residence, post office address and citizenship are as stated below next to my name.						
I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names ar listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:							
LOAD BALANCING METHOD AND SYSTEM BASED ON ESTIMATED ELONGATION RATES							
the specification of which is attached hereto unless the following box is checked:							
was filed onas United States Application Number or PCT International Application Number (if applicable).							
I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by an amendment referred to above. I acknowledge the duty to disclose information which is known by me to be material to patentability as defined in Title 37, Code of Federa Regulations § 1.56.							
I acknowledge the duty to disclose info Regulations § 1.56.	rmation	which is known by me to be t	naterial to	o paten	ntability as defined in Title 3	37, Code of Federa	
I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed: PRIOR FOREIGN APPLICATION(S)							
NUMBER	COUNTRY DAY/MONTH/YEAR FILED PRIORITY CLAIMED				PRIORITY CLAIMED		
285271/1999		Japan			06/10/1999	Yes	
Thereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below.							
APPLICATION NO. FILING DATE							
				7			
I hereby claim the benefit under Title 3 application designating the United State in the prior United States or PCT Inter 112, I acknowledge the duty to disclose Regulations § 1.56 which became avail of this application:	5, Unite s, listed national informa lable be	ed States Code, § 120 of any Un below and, insofar as the subject I application in the manner pro- ation which is known by me to be tween the filing date of the pri-	nited State of matter of vided by e material or applica	es appl of each the firs I to pate ation at	ication(s), or § 365(c) of an of the claims of this applica- st paragraph of Title 35, Ur- entability as defined in Title in ad the national or PCT Inte	y PCT Internationa tion is not disclosed itled States Code, is 37, Code of Federa rnational filing date	
APPLICATION SERIAL NO.		FILING DATE			STATUS: PATENTED ABANDONI	PENDING,	

I hereby appoint as my attorneys, with full powers of substitution and revocation, to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Stephen A. Bent, Reg. No. 29,768; David A. Blumenthal. Reg. No. 26,257; William T. Ellis, Reg. No. 26,874; John J. Feldhaus, Reg. No. 28,822; Patricia D. Granados, Reg. No. 33,683; John P. Isacson, Reg. No. 33,715; Donald D. Jeffery, Reg. No. 19,980; Eugene M. Lee, Reg. No. 32,039; Peter G. Mack, Reg. No. 26,001; Brian J. McNamara, Reg. No. 32,789; Sybit Meloy, Reg. No. 22,749; George E. Quillin, Reg. No. 32,792; Culin G. Sandercock, Reg. No. 31,298; Bernhard D. Saxe, Reg. No. 28,665; Charles F. Schill, Reg. No. 27,590; Richard L. Schwaab, Reg. No. 25,479; Arthur Schwartz, Reg. No. 22,115; Harold C. Wegner, Reg. No. 25,258.

2 004	/045
--------------	------

2000 10/05 THU 18:07	FAX 03 3288	3222 Ktsuragi Patent → F0	ILEY & LARDNER	Ø 004/0
		1487-14 /	Doeler Ma	

Address all correspondence to FOLEY & LARDNER, Washington Harbour, 3000 K Street, N.W., Suite 500, P.O. Box 25696, Washington, D.C. 20007-8696. Address telephone communications to _______ at (202) 672-5300.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of First or Sole Inventor		t or Sole Inventor	Date
HIDEHITO KUBO	Hidehite		02/10/2000
Residence Address		Country of Citizensh	ip
Tokyo, Japan		Japan	
Post Office Address			
c/o NEC Corporation, 7-1, Shiba 5-0	chome, Mina	to-ku, Tokyo,	Japan
			ليب ــــــــــــــــــــــــــــــــــــ
Full Name of Second Inventor	Signature of Seco	ond inventor	Date
			1
Residence Address	L	Country of Citizensh	ip
			1
Post Office Address			
1 nor Ortice United			
Full Name of Third Inventor	Signature of Thir	d Inventor	Date
Lent large of fully macrior.	Signature of 1011	IN THACHIOL	Date
# #			<u> </u>
Residence Address		Country of Citizensh	ip
#= #=			
Post Office Address			
Full Name of Fourth Inventor	Signature of Fou	rth Inventor	Date
Residence Address	<u> </u>	Country of Citizensh	ip
# # # # # # # # # # # # # # # # # # #		·	-
Post Office Address			,
rost Ottos Address			
,			
	Signature of Fift		Date
Full Name of Fifth Inventor	algnature of FIR.	n inventor	שוגעו
Residence Address		Country of Citizensh	ip
Post Office Address		L	